EC3101PC: MICROPROCESSORS AND MICROCONTROLLERS

B. Tech.   III   Year   I   Sem.   **L   T   P   C**

Prerequisite: Nil   **3   1   0   4**

Course Objectives:
1. To familiarize the architecture of microprocessors and micro controllers
2. To provide the knowledge about interfacing techniques of bus & memory.
3. To understand the concepts of ARM architecture
4. To study the basic concepts of Advanced ARM processors

Course Outcomes: Upon completing this course, the student will be able to
1. Understands the internal architecture, organization and assembly language programming of 8086 processors.
2. Understands the internal architecture, organization and assembly language programming of 8051/controllers
3. Understands the interfacing techniques to 8086 and 8051 based systems.
4. Understands the internal architecture of ARM processors and basic concepts of advanced ARM processors.

UNIT -I:
8086 Architecture: 8086 Architecture-Functional diagram, Register Organization, Memory Segmentation, Programming Model, Memory addresses, Physical Memory Organization, Architecture of 8086, Signal descriptions of 8086, interrupts of 8086.
Instruction Set and Assembly Language Programming of 8086: Instruction formats, Addressing modes, Instruction Set, Assembler Directives, Macros, and Simple Programs involving Logical, Branch and Call Instructions, Sorting, String Manipulations.

UNIT -II:
Introduction to Microcontrollers: Overview of 8051 Microcontroller, Architecture, I/O Ports, Memory Organization, Addressing Modes and Instruction set of 8051.
8051 Real Time Control: Programming Timer Interrupts, Programming External Hardware Interrupts, Programming the Serial Communication Interrupts, Programming 8051 Timers and Counters

UNIT –III:
I/O And Memory Interface: LCD, Keyboard, External Memory RAM, ROM Interface, ADC, DAC Interface to 8051.
Serial Communication and Bus Interface: Serial Communication Standards, Serial Data Transfer Scheme, On board Communication Interfaces-I2C Bus, SPI Bus, UART; External Communication Interfaces-RS232,USB.

UNIT –IV:
ARM Architecture: ARM Processor fundamentals, ARM Architecture – Register, CPSR, Pipeline, exceptions and interrupts interrupt vector table, ARM instruction set – Data processing, Branch instructions, load store instructions, Software interrupt instructions, Program status register instructions, loading constants, Conditional execution, Introduction to Thumb instructions.

UNIT – V:
Advanced ARM Processors: Introduction to CORTEX Processor and its architecture, OMAP Processor and its Architecture.

TEXT BOOKS:
1. Advanced Microprocessors and Peripherals – A. K. Ray and K. M. Bhurchandani, TMH, 2nd Edition 2006.
2. ARM System Developers guide, Andrew N SLOSS, Dominic SYMES, Chris WRIGHT, Elsevier, 2012

REFERENCE BOOKS:
1. The 8051 Microcontroller, Kenneth. J. Ayala, Cengage Learning, 3rd Ed, 2004.
2. Microprocessors and Interfacing, D. V. Hall, TMGH, 2nd Edition 2006.
3. The 8051 Microcontrollers, Architecture and Programming and Applications -K. Uma Rao, Andhe Pallavi, Pearson, 2009.
4. Digital Signal Processing and Applications with the OMAP- L138 Experimenter, Donald Reay, WILEY 2012.